

10/065202
09/28/02

16/512

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10065202	09/25/2002	716	7	2825	DIMYAN

**APPLICANTS: Bednar Thomas; Gould Scott; Lackey David; Stout Douglas; Zuchowski Paul;

**CONTINUING DATA VERIFIED:

WOM N-11

BEST AVAILABLE COPY

** FOREIGN APPLICATIONS VERIFIED:

WOM N-11

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☒ no

35 USC 119 conditions met ☐ yes ☒ no

Verified and Acknowledged Examiners's initials

M-11

ATTORNEY DOCKET NO

BUR920020057

TITLE : Voltage island design planning

U.S. DEPT. OF COMM./PAT & TM-PTO-436L/Rev. 12-94

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claims for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawn	Fig. Drawn
		Print Fig.	
<input type="checkbox"/> TERMINAL		Primary Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
		Application Examiner	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368; Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)